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PTO/SB/21 (08-00)

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<h1>TRANSMITTAL FORM</h1> <p>(to be used for all correspondence after initial filing)</p>	Applicati n Number	09/716,734	
	Filing Date	Nov. 20, 2000	
	First Named Inventor	Chan et al.	
	Group Art Unit	2826	
	Examiner Name	A. O. Williams	
Total Number of Pages in This Submission	5	Attorney Docket Number	00100.01.0012

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ENCLOSURES (check all that apply)		
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Remarks		

SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT	
Firm or Individual name	Christopher J. Reckamp Reg. No. 34,414
Signature	
Date	February 28, 2002

CERTIFICATE OF MAILING	
I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, Washington, DC 20231 on this date: <u>Feb 28, 2002</u>	
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PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Chan et al.  
Serial No. 09/716,734  
Filing Date: Nov. 20, 2000  
Confirmation No. 7999

Examiner: Alexander O. Williams  
Art Group: 2826  
Our file no. 00100.01.0012  
Docket No. 0100.0100120

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REC'D  
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3/22/02  
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**Title: INTEGRATED CIRCUIT PACKAGE AND METHOD OF FABRICATING SAME**

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2/28/02 Rosalie Swanson  
Date Rosalie Swanson

**RESPONSE TO RESTRICTION REQUIREMENT**

**In the Claims:**

Please amend Claim 21 as follows:

In particular, please substitute the below claim for the indicating pending claim with the same number:

21. (Once Amended) A method for fabricating an integrated circuit, the method comprising the steps of:

providing a first substrate, the first substrate including at least one heat-generating circuit and having a first coefficient of thermal expansion;

providing a second substrate, the second substrate having a second coefficient of thermal expansion that is substantially equal to the first coefficient of thermal expansion;

thermally coupling the first substrate to the second substrate, the second substrate thermally conducts heat generated by the at least one heat-generating circuit away from the at least one heat-generating circuit.